

RESUME

Name : Dr A VEERABHADRA REDDY
Designation : HECE
Department : ELECTRONICS AND COMMUNICATION ENGINEERING
Employee ID : 1001917
C. F. M. S. ID : 14246825
PAN : AEUPA9790F
AADHAAR : 384177195101
Blood Group : AB+
Date of Birth : 10-05-1965
Email ID : bhadrareddy47@gmail.com
Mobile Number : 9441066424
Address for Communication: HIG-6, APHB COLONY, NANDYAL -518502(AP)



Date of Initial appointment in the department: 05-07-1990

Date of joining at this institute: 19-01-2022

Total Teaching Experience: 35 YEARS

Educational Qualifications:

S.No.	Degree	University/Board/Institution	Year of Passing
1	Ph.D	JNTUA, ANANTAPURAMU	2015
2	M.Tech/M.E./M.Sc./M.A.	JNTU, HYDERABAD	2005
3	B.Tech/B.E./B.Sc./B.A.	BAPATLA ENGG COLLEGE, NAGARJUNA UNIVERSITY, GUNTUR	1988
4	INTERMEDIATE	GOVT. JUNIOR COLLEGE, KOVELAKUNTLA	1982
5	S.S.C.	DVSR Z.P.H.S, GOSPADU	1980

Details of Experience:

S.No.	Name of the Institution/Organization	Designation	From	To
1	UNITRON LTD., FARIDABAD	PRODUCTION ENGINEER	AUGUST 1988	MARCH 1989
2	KITS , RAMTEK, MAHARASTRA	ASST. PROFESSOR	JULY 1989	JUNE 1990
1.	GOVT POLYTECHNIC FOR WOMEN, HINDUPUR	LECTURER	05-07-1990	15-06-1995
2.	ESC GOVT POLYTECHNIC, NANDYAL	LECTURER	15-6-1995	31-7-2004
3	GOVT POLYTECHNIC, ANANTAPURAMU	LECTURER	2-8-2004	16-6-2008
4	ESC GOVT POLYTECHNIC, NANDYAL	LECTURER	18-6-2008	15-10-2009
5	GOVT POLYTECHNICFOR WOMEN, HINDUPUR	Senior Lecturer in ECE	19-10-2009	30-6-2012
6	ESC GOVT POLYTECHNIC, NANDYAL	HECE	1-7-2012	18-1-2022
7	GOVT POLYTECHNIC, PRODDATUR	HECE	19-01-2022	TILL DATE

Professional Memberships:

Continuous Learning: Karmayogi

Publications: 1 “An Agent based approach for End-to- -End QoS Guarantees in Multimedia IP Networks”
2.” Hybrid FEC/MEC based loss recovery in Multimedia IP Networks”
3”Multilevel down link relayQueue ware and loss recovery scheduling for media transimission in wireless cellular networks”
4.”A QoS centric layered transmission topologyfor multimedia datain wireless cellular networks”

Training Details:

S.No.	Name of the Programme	Conducted by	Duration and Dates
1.	Circuits and Measurements Lab Practice	NITTTR Chennai	16-02-2015 to 20-02-2015 (one week)
2	Advanced Communications	NITTTR Chennai	27-06-2016 to 01-07-2016 (one week)
3.	Circuit simulation and PCB design using Multisim	NITTTR Chennai	01-08-2016 to 05-08-2016 (one week)
4.	Special electrical machines and control	NITTTR Chennai	29-01-2018 to 02-02-2018 (one week)
5.	Induction Programme for newly recruited Teachers	NITTTR Chennai	16-04-2018 to 27-04-2018 (two weeks)
6.	Laboratory Instruction in communication	NITTTR Chennai	27-05-2019 to 01-06-2019 (one week)
7.	Refresher Course on Electronics	NITTTR Chennai	11-11-2019 to 22-11-2019 (two weeks)
8.	Laboratory Practice in Pspice	NITTTR Chennai	28-03-2022 to 01-04-2022 (one week)
9.	Embedded System Design and development	NITTTR Chennai	30-10-2023 to 03-11-2023 (one week)

Awards & Achievements:

1. Acted as Guest faculty at RGM Engineering College Nandyal, Alfa Engineering College Allagadda, Ramakrishna PG College Nandyal.
2. Acted as GATE Coaching faculty at SK University BC Cell.
3. Participated in NIPU Project Preparation.
4. Worked as Assistant Project Officer in CPS from 1992-1997.
5. Worked as OSD(Officer on Special Duty) to Government Polytechnic Dharmavaram from 2010-13.

Signature